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	MOTOROLA LAW DEPA	ARTMENT
DATE:	January 9, 2004	
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Docket No:	SC11697TP	
Applicant:	Rama Hedge et al.	
	10/098,706	
Art Unit:	2829	
Filed:	March 15, 2002	
Class:	438-591000	
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DOCKET NO. SC11697TP



UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)

Rama I. Hedge

GROUP ART UNIT:

2829

APPLN. NO.:

10/098,706

EXAMINER: Asok K.Sarkar

FILED:

March 15, 2002

TITLE:

GATE DIELECTRIC AND METHOD THEREFOR

LETTER TO EXAMINER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The title of the above-referenced application has been amended in accordance with Examiner's Amendment (see page 2 of the Notice of Allowability). The correct title is "TRANSISTOR WITH LAYERED HIGH-K GATE DIELECTRIC AND METHOD THEREFOR,"

Respectfully submitted,

SEND CORRESPONDENCE TO:

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